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1 [A formal model for reasoning about adaptive QoS-enabled middleware](#)



[Nalini Venkatasubramanian](#), [Carolyn Talcott](#), [Gul A. Agha](#)

January Transactions on Software Engineering and Methodology
 2004 (TOSEM) , Volume 13 Issue 1

Publisher: ACM

Full text available: Pdf (1.42 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 17, Downloads (12 Months): 154, Citation Count: 3

Systems that provide distributed multimedia services are subject to constant evolution; customizable middleware is required to effectively manage this change. Middleware services for resource management execute concurrently with each other, and with ...

Keywords: Middleware services, actors, meta-object models, multimedia, quality-of-service, reflection, theoretical foundations

2 [LyricAlly: automatic synchronization of acoustic musical signals and textual](#)



[lyrics](#)

[Ye Wang](#), [Min-Yen Kan](#), [Tin Lay Nwe](#), [Arun Shenoy](#), [Jun Yin](#)

October MULTIMEDIA '04: Proceedings of the 12th annual ACM international
 2004 conference on Multimedia

Publisher: ACM

Full text available: Pdf (485.10 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 60, Citation Count: 7

We present a prototype that automatically aligns acoustic musical signals with their corresponding textual lyrics, in a manner similar to manually-aligned karaoke. We tackle this problem using a multimodal approach, where the appropriate pairing of audio ...

Keywords: audio/text synergy, karaoke, lyric alignment, music knowledge, vocal detection

3 30 seconds is not enough!: a study of operating system timer usage



Simon Peter, Andrew Baumann, Timothy Roscoe, Paul Barham, Rebecca Isaacs

April Eurosys '08: Proceedings of the 3rd ACM SIGOPS/EuroSys European
2008 Conference on Computer Systems 2008

Publisher: ACM

Full text available: Pdf (10.59 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 17, Downloads (12 Months): 310, Citation Count: 0

The basic system timer facilities used by applications and OS kernels for scheduling timeouts and periodic activities have remained largely unchanged for decades, while hardware architectures and application loads have changed radically. This raises ...

Keywords: adaptability, kernel interface design, scheduling, timers

Also published in:

April 2008 **SIGOPS Operating Systems Review** Volume 42 Issue 4

4 Scheduler activations: effective kernel support for the user-level management of parallelism



Thomas E. Anderson, Brian N. Bershad, Edward D. Lazowska, Henry M. Levy

October SOSp '91: Proceedings of the thirteenth ACM symposium on Operating
1991 systems principles

Publisher: ACM

Full text available: Pdf (1.68 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 57, Downloads (12 Months): 259, Citation Count: 43

Threads are the vehicle for concurrency in many approaches to parallel programming. Threads separate the notion of a sequential execution stream from the other aspects of traditional UNIX-like processes, such as address spaces and I/O descriptors. ...

Also published in:

October 1991 **SIGOPS Operating Systems Review** Volume 25 Issue 5

5 Sharing and protection in a single-address-space operating system



Jeffrey S. Chase, Henry M. Levy, Michael J. Feeley, Edward D. Lazowska

November Transactions on Computer Systems (TOCS) , Volume 12 Issue 4
1994

Publisher: ACM

Full text available: Pdf (2.87 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 16, Downloads (12 Months): 276, Citation Count: 49

This article explores memory sharing and protection support in Opal, a single-address-space operating system designed for wide-address (64-bit) architectures. Opal threads execute within protection domains in a single shared virtual address space. Sharing ...

Keywords: 64-bit architectures, capability-based systems, microkernel operating systems, object-oriented database systems, persistent storage, protection, single-address-space operating systems, wide-address architectures

6 PROMPT: a mapping environment for telecom applications on "system-on-a-chip"



Michel Barreteau, Juliette Mattioli, Thierry Grandpierre, Christophe Lavarenne, Yves Sorel, Philippe Bonnot, Philippe Kaifasz

November 2000 CASES '00: Proceedings of the 2000 international conference on Compilers, architecture, and synthesis for embedded systems

Publisher: ACM

Full text available: Pdf (375.57 KB)

Additional Information: [full citation](#), [references](#)

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 20, Citation Count: 0

Keywords: constraint, embedded systems, heterogeneous multiprocessor, mapping environment, system-on-a-chip

7 The role of roles in supporting reconfigurability and fault localizations for open distributed and embedded systems



Shangping Ren, Yue Yu, Nianen Chen, Jeffrey J.-P. Tsai, Kevin Kwiat

September 2007 Transactions on Autonomous and Adaptive Systems (TAAS) , Volume 2 Issue 3

Publisher: ACM

Full text available: Pdf (913.95 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 12, Downloads (12 Months): 184, Citation Count: 0

One of the main characteristics of open distributed embedded systems is that the involved entities are often very dynamic—different individual entities may join or leave the systems frequently. Therefore, systems built of these dynamic entities ...

Keywords: Coordination, actors, coordinators, open distributed embedded systems, roles

8 [Proceedings of the 12th annual ACM international conference on Multimedia](#)



[Henning Schulzrinne](#), [Nevenka Dimitrova](#), [Angela Sasse](#), [Sue Moon](#), [Rainer Lienhart](#)

October 2004 MULTIMEDIA '04: Proceedings of the 12th annual ACM international conference on Multimedia

Publisher: ACM

Additional Information: [full citation](#), [abstract](#)

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 0

ACM Multimedia has traditionally been instrumental with its technical program to shape the field and give new directions for established conferences. Before the conference last year, the Special Interest Group on Multimedia (SIG MM) Chair Larry Rowe ...

9 [First-class user-level threads](#)



[Brian D. Marsh](#), [Michael L. Scott](#), [Thomas J. LeBlanc](#), [Evangelos P. Markatos](#)

October 1991 SOSP '91: Proceedings of the thirteenth ACM symposium on Operating systems principles

Publisher: ACM

Full text available: Pdf (1.53 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 10, Downloads (12 Months): 73, Citation Count: 33

It is often desirable, for reasons of clarity, portability, and efficiency, to write parallel programs in which the number of processes is independent of the number of available processors. Several modern operating systems support more than one process ...

Also published in:

October 1991 **SIGOPS Operating Systems Review** Volume 25 Issue 5

10 [Parallel discrete event simulation of space shuttle operations](#)

[Luís Rabelo](#), [Jose Sepulveda](#), [Mario Marin](#), [Amith Paruchuri](#), [Amit Wasadikar](#), [Karthik Nayanar](#)

December 2004 WSC '04: Proceedings of the 36th conference on Winter simulation

Publisher: Winter Simulation Conference

Full text available: Pdf (588.38 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 17, Citation Count: 0

This paper describes the application of parallel simulation techniques to represent structured functional parallelism present within the Space Shuttle Operations Flow, utilizing the Synchronous Parallel Environment for Emulation and Discrete-Event Simulation ...

11 Scheduler-conscious synchronization



Leonidas I. Kontothanassis, Robert W. Wisniewski, Michael L. Scott

February 1997 Transactions on Computer Systems (TOCS) , Volume 15 Issue 1

Publisher: ACM

Full text available: Pdf (682.20 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 82, Citation Count: 18

Efficient synchronization is important for achieving good performance in parallel programs, especially on large-scale multiprocessors. Most synchronization algorithms have been designed to run on a dedicated machine, with one application process per ...

Keywords: barriers, busy-waiting, kernel-user interaction, locks, mutual exclusion, preemption, scalability, scheduling, synchronization

12 Web-based analysis and distributed IP



Philip A. Wilsey

December 1999 WSC '99: Proceedings of the 31st conference on Winter simulation: Simulation---a bridge to the future - Volume 2 , Volume 2

Publisher: ACM

Full text available: Pdf (88.08 KB) Additional Information: [full citation](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 13, Citation Count: 1

13 Hybrid discrete event simulation with model predictive control for semiconductor supply-chain manufacturing

Hessam S. Sarioughian, Dongping Huang, Gary W. Godding, Karl G. Kempf, Wenlin Wang, Daniel E. Rivera, Hans D. Mittelmann

December 2005 WSC '05: Proceedings of the 37th conference on Winter simulation

Publisher: Winter Simulation Conference

Full text available: Pdf (416.67 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 74, Citation Count: 4

Simulation modeling combined with decision control can offer important benefits for analysis, design, and operation of semiconductor supply-chain network systems. Detailed simulation of physical processes provides information for its controller to account ...

14 [Algorithms for scalable synchronization on shared-memory multiprocessors](#)



[John M. Mellor-Crummey](#), [Michael L. Scott](#)

February Transactions on Computer Systems (TOCS) , Volume 9 Issue 1
1991

Publisher: ACM

Full text available: Pdf (3.07 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),
[index terms](#), [review](#)

Bibliometrics: Downloads (6 Weeks): 51, Downloads (12 Months): 526, Citation Count: 157

Busy-wait techniques are heavily used for mutual exclusion and barrier synchronization in shared-memory parallel programs. Unfortunately, typical implementations of busy-waiting tend to produce large amounts of memory and interconnect contention, introducing performance ...

15 [VLE: a multimodeling and simulation environment](#)

[Gauthier Quesnel](#), [Raphaël Duboz](#), [Éric Ramat](#), [Mamadou K. Traoré](#)

July SCSC: Proceedings of the 2007 summer computer simulation conference
2007

Publisher: Society for Computer Simulation International

Full text available: Pdf (684.24 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 45, Citation Count: 0

Modeling and Simulation (M&S) is becoming the core of scientific activities addressing nature complexity. To tackle complexity, we need to integrate heterogeneous formalisms in the same model. Models then become multimodels. Furthermore, M&S softwares ...

Keywords: DEVS extensions, modeling and simulation cycle, multimodeling

16 [Grimage: markerless 3D interactions](#)



[Jérémie Allard](#), [Clément Menier](#), [Bruno Raffin](#), [Edmond Boyer](#), [François Faure](#)

August 2007 SIGGRAPH '07: SIGGRAPH 2007 emerging technologies

Publisher: ACM

Full text available: Pdf (15.50 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 97, Citation Count: 2

Grimage glues multi-camera 3D modeling, physical simulation and parallel execution for a new immersive experience. Put your hands or any object into the interaction space. It is instantaneously modeled in 3D and injected into a virtual world populated ...

Keywords: 3D interactions, PC cluster, markerless 3D modeling, multi-cameras, soft objects simulation

17 Parsing and interpreting ambiguous structures in spatial hypermedia



Luis Francisco Revilla, Frank Shipman

September 2005 HYPERTEXT '05: Proceedings of the sixteenth ACM conference on Hypertext and hypermedia

Publisher: ACM

Full text available: Pdf (663.83 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 56, Citation Count: 1

When reflecting on information, spatial hypermedia users express their understanding of the information's structure visually. In order to facilitate this process, spatial hypermedia uses spatial parsers that enable systems to infer the structure of information ...

Keywords: spatial hypertext, spatial parsers

18 Models and languages for parallel computation



David B. Skillicorn, Domenico Talia

June 1998 Computing Surveys (CSUR) , Volume 30 Issue 2

Publisher: ACM

Full text available: Pdf (298.05 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 52, Downloads (12 Months): 614, Citation Count: 54

We survey parallel programming models and languages using six criteria to assess their suitability for realistic portable parallel programming. We argue that an ideal model should be easy to program, should have a software development methodology, should ...

Keywords: general-purpose parallel computation, logic programming languages, object-oriented languages, parallel programming languages, parallel programming models, software development methods, taxonomy

19 Scheduler activations: effective kernel support for the user-level management of parallelism



Thomas E. Anderson, Brian N. Bershad, Edward D. Lazowska, Henry M. Levy

February 1992 Transactions on Computer Systems (TOCS) , Volume 10 Issue 1

Publisher: ACM

Full text available: Pdf (2.04 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)




Bibliometrics: Downloads (6 Weeks): 10, Downloads (12 Months): 159, Citation Count: 65

Threads are the vehicle for concurrency in many approaches to parallel programming. Threads can be supported either by the operating system kernel or by user-level library code in the application address space, but neither approach has ...

Keywords: multiprocessor, thread

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